



University Program Software Selection

University Program 2019

Level One

<u>Product</u>	<u>Product No.</u>	<u>Release Stream</u>
Conformal - GXL	CFM300	CONFRML181
Conformal Constraint Design – L	CFM401	CONFRML181
Conformal Constraint Designer - XL	CFM421	CONFRML181
CCD Multi-Constraint Check Option	CFM422	CONFRML181
Conformal Low Power - XL	CFM500	CONFRML181
Conformal Low Power – GXL	CFM550	CONFRML181
Conformal ECO Designer – GXL	CFM650	CONFRML181
Virtuoso® Quantus QRC Extraction - XL	QRCX300	EXT182
Virtuoso® Quantus QRC Advanced Analysis GXL option	QRCX310	EXT182
Cadence® Quantus QRC Advanced Modeling GXL Option	QRCX320	EXT182
Cadence® Quantus QRC Display Technology Option	QRCX330	EXT182
Cadence® Quantus QRC Advanced Modeling20 GXL Option	QRCX520	EXT182
Cadence® Quantus QRC Advanced Node Modeling Option	QRCX530	EXT182
Genus Low Power Option	GEN30	GENUS181
Genus Physical Option	GEN40	GENUS181
Genus CPU Accelerator Option	GEN80	GENUS181
Genus Synthesis Solution	GEN100	GENUS181
Cadence® Framework Integration Runtime Option	117	IC618
Virtuoso® Simulation Environment	206	IC618
Virtuoso® Schematic Editor HSPICE Interface	276	IC618
Dracula® Graphical User Interface	365	IC618
Cadence® SKILL Development Environment	900	IC618
Virtuoso® EDIF 200 Reader	940	IC618
Virtuoso® EDIF 200 Writer	945	IC618
Cadence® Design Framework Integrator's Toolkit	12141	IC618
Virtuoso® Schematic VHDL Interface	21060	IC618
Virtuoso® Schematic Editor Verilog Interface	21400	IC618
Virtuoso® Analog Oasis Run-Time Option	32100	IC618
Cadence® OASIS for RFDE	32101	IC618
Virtuoso® analog HSPICE HSPICE Interface	32760	IC618
Virtuoso® AMS Designer Environment	70000	IC618
Dracula® Physical Verification and Extractor Suite	70520	IC618
Diva® Physical Verification and Extractor Suite	71520	IC618
Virtuoso® Schematic Editor – XL	95115	IC618
Virtuoso® Analog Design Environment – XL	95210	IC618



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Virtuoso® Analog Design Environment – GXL	95220	IC618
Virtuoso® ADE Explorer	95250	IC618
Virtuoso® Visualization & Analysis XL	95255	IC618
Virtuoso® ADE Assembler	95260	IC618
Virtuoso® Variation Option	95265	IC618
Virtuoso® ADE Verifier	95270	IC618
Virtuoso® DFM Option	95311	IC618
Virtuoso® Layout Suite - GXL	95323	IC618
Virtuoso® Implementation Aware Design Option	95510	IC618
Virtuoso® System Design Platform	95541	IC618
Virtuoso® Layout Suite EAD	95600	IC618
Voltus-Fi Custom Power Integrity Solution - XL	VT500	IC618
Virtuoso® Advanced Node Framework	95011	ICADV123
Incisive Functional Safety Simulator	26262	INCISIV152
Verifault – XL Simulator	26500	INCISIV152
Verifault – XL Slave Node License	26510	INCISIV152
Enterprise Simulator - XL Interface for MTI	29661	INCISIV152
Enterprise Simulator - XL Interface for VCS	29671	INCISIV152
Virtuoso® Digital Implementation	3002	INNOVUS181
Innovus 20/16/14nm Option	INVS20	INNOVUS181
Innovus Mixed Signal Option	INVS30	INNOVUS181
Innovus High Frequency Route Option	INVS35	INNOVUS181
Innovus Hierarchical Design Option	INVS40	INNOVUS181
Innovus CPU Accelerator Option	INVS80	INNOVUS181
Innovus Implementation System	INVS100	INNOVUS181
JasperGold Automatic Formal Linting App	JGAFL100	JASPER1809
JasperGold Coverage APP Option	JGCOV100	JASPER1809
JasperGold Formal Property Verification APP	JGFPV100	JASPER1809
JasperGold Interactive Option	JGINT100	JASPER1809
JasperGold Formal Verification Platform	JGFVBASE	JASPER1809
JasperGold Sequential Equivalency Checking APP	JGSEC100	JASPER1809
JasperGold Coverage Unreachability App	JGUNR100	JASPER1809
JasperGold Verification Apps Option (Option to JGFVBASE)	JGVAO100	JASPER1809
Joules RTL Power Solution	JLS100	JLS181



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Virtuoso® Liberate Server	ALT110	LIBERATE181
Virtuoso® Liberate Client	ALT111	LIBERATE181
Virtuoso® Liberate LV Server	ALT610	LIBERATE181
Virtuoso® Liberate LV Client	ALT611	LIBERATE181
Modus DFT Option	MOD30	MODUS181
Modus Hierarchical Option	MOD60	MODUS181
Modus ATPG	MOD200	MODUS181
Virtuoso® LDE analyzer Option	95230	MVS181
Innovus DFM Option	INVS50	MVS181
Litho Electrical Analyzer	LEA108	MVS181
Litho Physical Analyzer	LPA108	MVS181
Distributed Process for 8 CPUs	LPA109	MVS181
Cadence® Litho Hotspot Fixing Option	LPA120	MVS181
Generator for Assura™ compatible verification decks	PASASG	PAS31
Generator for Diva® compatible verification decks	PASDIG	PAS31
Error Cell Generator	PASECG	PAS31
Graphical Technology Editor	PASGTE	PAS31
Pcell Generator	PASPCG	PAS31
Cadence® QuickView Layout and Mask Data Viewer	K2200	PEGASUS183
Cadence® QuickView Layout Data Viewer	K2210	PEGASUS183
Cadence® QuickView Sign-off Data Analysis Environment	K2211	PEGASUS183
Cadence® Physical Verification System Design Rule Checker XL	96210	PVS161
Cadence® Physical Verification System Layout vs. Schematic Checker XL	96220	PVS161
Cadence® Physical Verification System Programmable Electrical Checker	96230	PVS161
Cadence® Physical Verification System Programmable Electrical Checker XL	96235	PVS161
Cadence® Physical Verification System Results Manager	96240	PVS161
Cadence® Physical Verification System Design Analysis Option	96245	PVS161
Cadence® Physical Verification System QuickView Signoff Environment	96246	PVS161
Cadence® Physical Verification System Constraint Validator	96300	PVS161
Cadence® Physical Verification System Constraint Validator XL	96305	PVS161
Cadence® Physical Verification System Advanced Analysis Option	96320	PVS161
Cadence® Physical Verification System Advanced Device Option	96330	PVS161
Cadence® Physical Verification System Pattern Matching Option	96340	PVS161
Cadence® Physical Verification System Mask Rule Check Option	96350	PVS161
Cadence® Physical Verification System Results Viewer	96360	PVS161
Virtuoso® Integrated Physical Verification System Option for Layout Suite	96400	PVS161



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Allegro [®] Sigrity SI Base	PA5700	SIGRITY2018
Allegro [®] Sigrity PI Base	PA5800	SIGRITY2018
Allegro [®] Sigrity Power Aware SI Option	SIGR915	SIGRITY2018
Allegro [®] Sigrity PKG-PCB SSO Voltus Suite	SIGR925	SIGRITY2018
Allegro [®] Sigrity System Serial Link Option	SIGR935	SIGRITY2018
Allegro [®] Sigrity Package Assessment and Extraction Option	SIGR945	SIGRITY2018
Allegro [®] Design Authoring High-Speed Option	PA1410	SPB172
Allegro [®] Design Authoring Multi-Style Option	PA1510	SPB172
Allegro [®] Design Authoring Team Design Option	PA1720	SPB172
Allegro [®] PCB Designer	PA3100	SPB172
Allegro [®] PCB High-Speed Option	PA3110	SPB172
Allegro [®] PCB Miniaturization Option	PA3120	SPB172
Allegro [®] PCB Symphony Team Design Option	PA3160	SPB172
Allegro [®] PCB Team Design Option	PA3410	SPB172
Allegro [®] PCB Analog/RF Option	PA3420	SPB172
Allegro [®] PCB Design Planning Option	PA3670	SPB172
Allegro [®] Pspice Systems Option	PA4500	SPB172
Cadence [®] 3D Design Viewer	PA6605	SPB172
Allegro [®] 2 FPGA System Planner Option	PA8250	SPB172
Allegro [®] ASIC Prototyping with FPGA's	PA8630	SPB172
Allegro [®] AMS Simulator ¹	PS2200	SPB172
Allegro [®] PCB Routing Option	PS3500	SPB172
Allegro [®] PCB Librarian - XL	PX3500	SPB172
Allegro [®] Physical Viewer	PX3600	SPB172
OrbitIO	SIGR106	SPB172
Cadence [®] SiP Layout – XL	SIP225	SPB172
Spectre Characterization Simulator Option	3500	SPECTRE181
Spectre [®] RelXpert Reliability Simulator	33580	SPECTRE181
Interactive mode for Spectre [®] using Python/TCL	38530	SPECTRE181
Spectre AMS Designer	70070	SPECTRE181
Spectre Multi-Mode Simulation with AMS	90005	SPECTRE181
Virtuoso [®] Multi-mode Simulation Power Option	91400	SPECTRE181
Virtuoso [®] Multi-mode Simulation CPU Accelerator option	91500	SPECTRE181
Spectre Extensive Partitioned Simulator	91600	SPECTRE181

¹ superset of Pspice and Advanced Analysis Option



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Tempus Timing Signoff Solution – L	TPS100	SSV181
Tempus Timing Signoff Solution – XL	TPS200	SSV181
Tempus Timing Signoff Solution TSO	TPS300	SSV181
Tempus Timing Signoff Solution MP	TPS400	SSV181
Voltus IC Power Integrity Solution-XL (VTS-XL)	VTS200	SSV181
Voltus IC Power Integrity Solution Adv Analysis GXL Option (VTS-AA)	VTS201	SSV181
Voltus- IC Power Integrity – MP	VTS300	SSV181
Stratus HLS – XL	STR101	STRATUS181
vManager Project Server	VMG100	VMANAGER1809
vManager Linux Client	VMG005	VMANAGER1809
Indago Embedded Software Debug App	29862	XCELIUM1809
Xcelium Single Core	X300	XCELIUM1809
Xcelium Digital Mixed Signal Option	X310	XCELIUM1809



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Changes made for this year:

Discontinued products		Replacement products	
29651	Incisive™ Enterprise Simulator – XL	X300	Xcelium Single-Core
29710	Digital Mixed Signal Option to IES	X310	Xcelium Digital Mixed Signal Option
29851	Incisive™ Advanced Option	X300	Xcelium Single-Core
29861	Incisive™ Low-Power Simulation Option	X300	Xcelium Single-Core
25010	Cadence Simulation Analysis Environment	29862	Indago Embedded Software Debug App
23560	Incisive™ Formal Verifier	JGFVBASE	JasperGold Formal Verification Platform
IEV101	Incisive™ Enterprise Verifier – XL	JGFVBASE	JasperGold Formal Verification Platform
JGXPR100	JasperGold X-Propagation Verification APP	JGVAO100	JasperGold Verification Apps Option (Option to JGFVBASE)
JGCON100	JasperGold Connectivity Verification APP	JGVAO100	JasperGold Verification Apps Option
JGCSR100	JasperGold CSR Verification APP	JGVAO100	JasperGold Verification Apps Option
JGXPROPT	JasperGold X-Propagation Verification APP	JGFVBASE	JasperGold Formal Verification Platform
JGCONOPT	JasperGold Connectivity Verification APP	JGFVBASE	JasperGold Formal Verification Platform
JGCSROPT	JasperGold CSR Verification APP	JGFVBASE	JasperGold Formal Verification Platform
JGFPVOPT	JasperGold Formal Property Verification APP	JGFVBASE	JasperGold Formal Verification Platform

New products added this year:

38530	Interactive mode for Spectre(R) using Python/TCL
96360	Cadence Physical Verification System Results Viewer
JGFVBASE	JasperGold Formal Verification Platform